SystemC AMS & COSEDA User Group Meeting Program 2025

Date: Wednesday, December 3rd and Thursday, December 4th 2025

Location: This year's meeting will take place as an online event. The meeting will be scheduled in Central European Time (CET).

First Day: Wednesday, December 3rd 2025

Session 1

13:00 – 13:20 **Welcome Speech & COSEDA Technologies Update**
Thomas Hartung, COSEDA Technologies GmbH

13:20 – 13:45 **What`s new in COSIDE® 3.4 & Forecast for 2026**
Karsten Einwich, COSEDA Technologies GmbH

13:45 – 14:10 **COSIDE® Scripting Update 2025**
Paul Ehrlich, COSEDA Technologies GmbH

14:10 – 14:35 **Automated Project Setup using COSIDE® Scripting**
Alexandra Kuester, Bosch Sensortec GmbH

Session 2

14:45 – 15:10 **Bridging Power and RF: Smart Gate Driver Modeling in COSIDE® with SystemC AMS**
Guillermo Conde, Infineon Technologies AG

15:10 – 15:35 **Building Ambient Intelligence at the Sensor Edge: SystemC AMS Meets Modern Spiking Neural Processors**
Aditya Dalakoti & Rui Teixeira, Innatera Nanosystems BV

15:35 – 16:00 **SystemC/HLS Prototyping of UCIe Interfaces**
Adam Alhousiki & Thierry Tambe, Stanford University

Session 3

16:15 – 16:40 **Simulating Heterogeneous Chiplet-Based Architectures: Challenges and Approaches inside COSIDE®**
Steven Prigg, Hyperstone GmbH a Swissbit Company

16:40 – 17:05 **Workload Modeling with SystemC TLM and Debugging Virtual Prototypes in COSIDE®**
Thilo Voertler, COSEDA Technologies GmbH

17:05 – 17:30 **TLM Model Usage for Analysis of Data Transport in Complex SoC Structures**
Rocco Jonack, Arteris, Inc.

17:30 – 17:55 **RISC-V VP++: Fast Open-Source Virtual Platforms - Powering Research Innovation and Industrial Use**
Daniel Große, Johannes Kepler University Linz

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Second Day: Thursday, December 4th 2025

Session 4

09:00 – 09:35 **Multicore Support with VirtualGTM5 in COSIDE®**
Juergen Hanisch, Robert Bosch GmbH

09:35 – 09:55 **Multicore Debugging for GTM Virtual Prototype**
Matthias Noack, PLS Programmierbare Logik & Systeme GmbH

09:55 – 10:15 **Virtual GTM Model Verification**
Andrey Morozov, Robert Bosch GmbH

10:15 – 10:40 **COSIDE® support for VirtualGTM5**
Karsten Einwich, COSEDA Technologies GmbH

Session 5

11:00 – 11:25 **Ultra-Wideband System Simulation Using SystemC AMS in Industrial Environment**
Carna Zivkovic, NXP Semiconductors

11:25 – 11:50 **From Abstract Model to RTL: Automated Formal Assertions for Correct-by-Construction Design**
Osama Ayoub, LUBIS EDA GmbH

11:50 – 12:15 **Smarter Documentation with COSIDE®**
Thomas Arndt, COSEDA Technologies GmbH

12:15 – 12:30 Wrap-up & Farewell