

# SystemC AMS & COSEDA User Group Meeting Program 2021



**Date:** Tuesday - November 09, 2021

**Location:** This year's meeting will take place as an online event

## Session 1

09:00 – 09:15 **Welcome Speech & COSEDA Technologies Update**

*Thomas Hartung, COSEDA Technologies GmbH*

09:15 – 09:45 **What's new in COSIDE® 2.8 & Forecast for 2022**

*Karsten Einwich, COSEDA Technologies GmbH*

09:45 – 10:15 **AMS Timing Closure with COSIDE®**

*Guillermo Conde, Infineon Technologies*

## Session 2

11:00 – 11:30 **Tutorial: Virtual Port Grouping with COSIDE®**

*Thomas Arndt, COSEDA Technologies GmbH*

11:30 – 12:00 **SystemVerilog-like Assertions in SystemC**

*Muhammad Hassan, German Research Center for Artificial Intelligence (DFKI)*

12:00 – 12:30 **High Level Digital System Design with COSIDE® using MatchLib and Catapult® HLS**

*Petri Solanti, Siemens Electronic Design Automation GmbH*

## Session 3

14:00 – 14:30 **Integrating ARM Fast and Cycle Models using COSIDE®**

*Thilo Voertler, COSEDA Technologies GmbH*

14:30 – 15:00 **Brushless PM Motor Control using Cycle Accurate ARM Model in COSIDE®**

*Hayri Verner Hasou, Infineon Technologies Italia S.r.l.*

15:00 – 15:30 **Tutorial: HDL Integration in COSIDE®**

*Paul Ehrlich, COSEDA Technologies GmbH*

## Session 4

16:00 – 16:30 **System-level Uncertainty Quantification using mixed-signal Models**

*Gabor Karsai, Vanderbilt University (Nashville, TN)*

16:30 – 17:00 **Mixed-Signal-Virtual Prototyping with COSIDE® of Power Converter Systems  
invoking AURIX™ VP**

*Radovan Vuletic, Infineon Technologies*

17:00 – 17:30 **COSIDE® - New Released and Future COSIDE® Features**

*Karsten Einwich, COSEDA Technologies GmbH*

**17:30 – 18:00 Wrap-up & Farewell**