

SystemC AMS & COSIDE® User Group Meeting Program 2018



Date: Friday - October 26, 2018

Location: Maritim Hotel Munich, Goethestrasse 7, 80336 Munich, Germany
(Located at the main station)

09:00 **Registration & Welcome Coffee**

09:15 – 09:30 **Welcome Speech & COSEDA Technologies Update**

Thomas Hartung, COSEDA Technologies GmbH

09:30 – 10:00 **What`s new in COSIDE® 2.5 & Forecast for 2019**

Karsten Einwich, COSEDA Technologies GmbH

10:00 – 10:25 **Requirement Tracking with COSIDE®**

Paul Ehrlich, COSEDA Technologies GmbH

10:25 – 10:55 **Coffee Break**

10:55 – 11:20 **Bridging the Gap between Analog and Firmware**

Rainer Findenig, DICE Danube Integrated Circuit Engineering GmbH & Co. KG

11:20 – 11:45 **COSIDE® High Level Synthesis Support**

Thomas Arndt, COSEDA Technologies GmbH

11:45 – 12:10 **From SystemC to FPGA, a Smooth Transition from COSIDE® to real Hardware**

Simone Fontanesi & Gaetano Formato Infineon Technologies AG

12:10 – 13:40 **Lunch Break & Networking**

13:40 – 14:20 **Tutorial: COSIDE® RF Modelling**

Karsten Einwich, COSEDA Technologies GmbH

14:20 – 14:45 **New Architecture Exploration for Radio Design using COSIDE®**

Cyril Voillequin & Quentin Devisse, Thales Communications & Security S.A.S

14:45 – 15:10 **Frac-N PLL with SD Modulator in SystemC AMS**

Joen Westendorp, NXP Semiconductors

15:10 – 15:40 **Coffee Break**

15:40 – 16:05 **Optimization of Complex Circuits at System Level**

Dr. Frank C. Schenkel, MunEDA GmbH

16:05 – 16:30 **Tutorial: COSIDE® Statistical Simulation**

Dr. Thilo Vörtler, COSEDA Technologies GmbH

16:30 – 17:00 **Demo`s & Wrap-up & Farewell**